AS3360

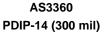
AS3360 - Dual Voltage Controlled Amplifier (VCA)

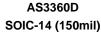
FEATURES

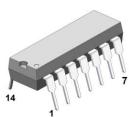
- Two Independent VCAs in a Single 14 Pin package
- Simple to Use Few External Components Required
- Exceptionally Low Control Feedthrough Without Trimming: 10mV Maximum Out of 10 V.P.P. Output
- Low Noise: -110 dB Typical
- No Trimming Required
- Summing Node Signal Inputs
- Current Outputs Capable of Swinging to Within 1.5V of Each Supply
- Linear and Exponential Control
- Control Voltages Referenced to Ground
- Wide Supply Range: ±3 to ±12V or +15, -3 to -9 V

APPLICATIONS

for electronic music









General Description

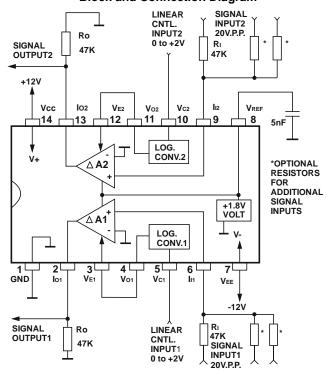
The AS3360 is a dual general purpose voltage controlled transconductor intended for such applications as voltage controlled amplifiers, filters, and waveform generators. Each transconductor independently provides both linear and exponential control scaling over greater than a 100 dB range. Complete with virtual ground summing inputs, wide voltage compliance current outputs, and control inputs referenced to ground, the AS3360 requires exceptionally few external components and is extremely easy to use.

Because of its inherent ultra-low control feedthrough, no trimming is required. Added to these features are exceptionally low noise, wide bandwidth, and operation down to \pm 3 volts, making the AS3360 a real cost saver in most applications requiring variable transconductance amplifiers.

Pin Information

PDIP-14 SOIC-14 Pin No	Pin Name	Description		
1	GND	Ground		
2	I _{O1}	Signal Ouput1		
3	V _{E1}	Control Voltage1		
4	V _{O1}	Log Converter Output1		
5	V _{C1}	Linear Cntl. Input		
6	I _{I1}	Current Input1		
7	V_{EE}	Negative Power		
8	V_{REF}	Reference Voltage Input		
9	I ₁₂	Current Input2		
10	V _{C2}	Linear Cntl. Input2		
11	V_{O2}	Log Converter Output2		
12	V _{E2}	Control Voltage2		
13	I _{O2}	Signal Ouput2		
14	Vcc	Positive Power		

Block and Connection Diagram



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Absolute Maximum Ratings

Typical Electrical Characteristics

 $V_{CC} = +12V \quad V_{EE} = -12V \quad T_{A} = 20^{\circ}C$

V_{CC} =+12 V_{EE} = -12 V_{CC} T _A = 20°C					
Parameter	Min.	Тур.	Max.	Units	
Control Range, Linear and Exponential	100	-	-	dB	
Control Scale Factor					
Exponential ¹	+ 2.7	+ 3.0	+ 3.3	mV/dB	
Linear	48	52	56	%/V	
Tempco ot Control Scales					
Exponential	+ 3000	+ 3300	+ 3600	ppm	
Linear	-	±250	±750	ppm	
Control Scale Error					
Exponential ²	-	0.6	2	dB	
Linear	-	3.0	6.0	%	
Maximum Cell Current Gain ³	0.9	1.0	1.1		
Maximum Signal Input and Output Current	±300	±400	±500	μΑ	
Signal Input Offset	-10	0	+ 10	mV	
Control Feedthrough Without Trim ⁴	-	±0.07	±0.3	μΑ	
Total Harmonic Distortion ³	-	1.0	3.0	%	
Output Noise Current ⁵	-	0.4	1.2	nA.R.M.S.	
Signal Current Bandwidth	2.0	5.0	•	MHz	
Signal Current Slew Rate ³	0.5	1.5	-	mA/μS	
Crosstalk Between VCAs ⁶	-80	-90	•	dB	
Signal Attenuation for Linear Control Input = 0V ⁷	70	80	-	dB	
Linear Control Voltage for Maximum Gain	1.6	1.7	1.8	V	
Exponential Control Voltage Range,					
Referred to VREF (Pin 8)	+ 20	-	-280	mV	
Control Input Bias Current					
Exponential ³	-0.3	-0.8	-1.5	μΑ	
Linear	-0.5	-1.6	-4	μΑ	
Output Impedance ³	5	12	-	M ohm	
Output Voltage Compliance ³	V _{EE} +1.2	-	Vcc-0.8	V	
Reference Voltage (Pin 8)	1.6	1.7	1.8	V	
Positive Supply Voltage Range 8	+3	-	+16	V	
Negative Supply Voltage Range 8	-3	-	-16	V	
Supply Current	3.8	4.8	6	mA	

- Note 1. Current gain is 20dB to 80dB. Control voltage is referenced to pin 8.
- Note 2. Best straight line. Most of this error occurs at range extremities.
- Note 3. Output Signal Current is ±100µA.
- Note 4. Over entire control range. Signal input is open.
- Note 5. In 16 to 16KHz bandwidth.
- Note 6. At 1KHz.
- Note 7. For negative supply less than 12 volts, this attenuation is greater.
- Note 8. Total supply voltage across chip should not exceed 26V.

Specifications subject to change without notice.

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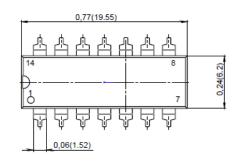
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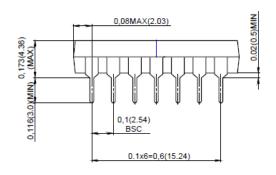
Device type	Package		
AS3360	PDIP-14 (300 Mil)		
AS3360D	SOIC-14 (150 Mil)		

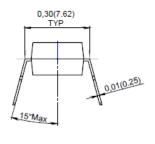
Package Information

Units: inch (mm)

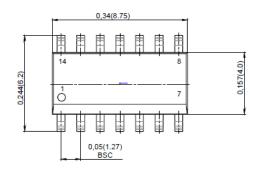
PDIP-14 (300 mil)

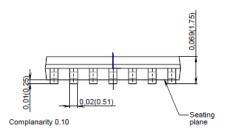


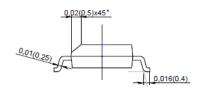




SOIC-14 (150 mil)







Revision history

Date	Revision	Changes
27-Sep-2017	1	Preliminary version 1
21-May-2018	2	The control voltage range at the linear control input and Reference Voltage are adjusted
30-May-2018	3	Minor changes



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